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1. Introduction

1.1 How to Use This Guide

This guide is an ideal starting point for those new to capacitive touch sensing (CapSense®) as well as for learning key design considerations and layout best practices to ensure design success. In addition, you can use this guide to:

- Become familiar with the technology underlying CapSense solutions
- Understand important design considerations, such as layout, schematic, and EMI (Electro Magnetic Interference)
- Become familiar with the CapSense product portfolio
- Select the right device for your application
- Migrate between CapSense devices
- Become familiar with the many resources available to support your entire design cycle

When you are ready to design your application, consult the Design Guide specific to the CapSense device family you have selected.

1.2 Cypress’s CapSense Documentation Ecosystem

Figure 1-1 and Table 1-1 summarize the Cypress CapSense documentation ecosystem. These resources allow you to quickly access the information needed to complete a CapSense product design successfully. Figure 1-1 shows the typical flow of a product design cycle with capacitive sensing; the information in this guide is highlighted in green. Table 1-1 provides links to the supporting documents for each of the numbered tasks in Figure 1-1.
Figure 1-1. Typical CapSense Product Design Flow

1. Capacitive touch sensing basics
2. Specify system requirements and characteristics
3. CapSense device selection based on needed functionality
   - Topics covered in this document
   - = Applicable for CY80MBR2xxx family of devices
   † = Applicable for CapSense and CapSense Plus programmable devices

Design for CapSense

4. Mechanical design
5. Schematic capture and PCB layout
6. PSoC Designer project creation†
   7. Firmware development†
   8. CapSense tuning†

9. Programming PSoC

10. CapSense configuration*

11. Preproduction build (prototype)

12. Test and evaluate system functionality and CapSense performance

  Performance satisfactory
  -- No
  -- Yes

13. Production
Table 1-1. Cypress Documents Supporting Numbered Design Tasks of Figure 1-1

<table>
<thead>
<tr>
<th>Figure 1-1 Task No.</th>
<th>Supporting Cypress CapSense Documentation</th>
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<tbody>
<tr>
<td>1</td>
<td>Getting Started with CapSense</td>
</tr>
<tr>
<td>2</td>
<td>Getting Started with CapSense</td>
</tr>
<tr>
<td></td>
<td>Device Family Specific CapSense Device Datasheets</td>
</tr>
<tr>
<td></td>
<td>Device Family Specific CapSense Design Guides</td>
</tr>
<tr>
<td>3</td>
<td>Getting Started with CapSense</td>
</tr>
<tr>
<td>4</td>
<td>Getting Started with CapSense</td>
</tr>
<tr>
<td>5</td>
<td>Getting Started with CapSense</td>
</tr>
<tr>
<td>6</td>
<td>PSOC Designer User Guides</td>
</tr>
<tr>
<td>7</td>
<td>Assembly Language User Guide</td>
</tr>
<tr>
<td></td>
<td>C Language Compiler User Guide</td>
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<tr>
<td></td>
<td>CapSense Code Examples</td>
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<td></td>
<td>Device Family Specific Technical Reference Manuals</td>
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<td>Device Family Specific CapSense Design Guides</td>
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<td></td>
<td>Device Family Specific CapSense User Module Datasheets (CSA_EMU)</td>
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<td></td>
<td>CapSense Data Viewing Tools -AN2397</td>
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<td></td>
<td>CapSense Controller Code Examples Design Guide</td>
</tr>
<tr>
<td>9</td>
<td>Programmer User Guide</td>
</tr>
<tr>
<td>10</td>
<td>MiniProg3 User Guide</td>
</tr>
<tr>
<td>11</td>
<td>CapSense Code Examples</td>
</tr>
</tbody>
</table>

1.3 Cypress CapSense Products

Cypress CapSense solutions bring elegant, reliable, and easy-to-use capacitive touch sensing functionality to your design. Our capacitive touch sensing solutions have replaced more than four billion mechanical buttons. Capacitive touch sensing has changed the face of industrial design in products, such as cell phones, PCs, consumer electronics, automotive features, and white goods. Cypress’s robust CapSense solutions leverage our flexible Programmable System-on-Chip (PSoC®) architecture, which accelerates time-to-market, integrates critical system functions, and reduces BOM costs.

1.3.1 Cypress CapSense Differentiation

- Robust sensing technology
- High noise immunity
- High performance sensing across a variety of overlay materials and thicknesses
- SmartSense™ Auto-Tuning technology
- Proximity sensing
- Water tolerant operation
- Complete user interface solution including audio, visual, and haptics feedback
- Low power consumption
- Wide operating voltage range (1.71—5.5 V)
- Small form factor packaging
- Reduced BOM cost with integrated CapSense Plus features (ADC, DAC, timer, counter, PWM)
# 1.4 Document Conventions

<table>
<thead>
<tr>
<th>Convention</th>
<th>Usage</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Courier New</strong></td>
<td>Displays file locations, user entered text, and source code:</td>
</tr>
<tr>
<td></td>
<td>C:...cd\icc\</td>
</tr>
<tr>
<td><strong>Italics</strong></td>
<td>Displays file names and reference documentation:</td>
</tr>
<tr>
<td></td>
<td>Read about the sourcefile.hex file in the PSoC Designer User Guide.</td>
</tr>
<tr>
<td><strong>[Bracketed, Bold]</strong></td>
<td>Displays keyboard commands in procedures:</td>
</tr>
<tr>
<td></td>
<td>[Enter] or [Ctrl] [C]</td>
</tr>
<tr>
<td><strong>File &gt; Open</strong></td>
<td>Represents menu paths:</td>
</tr>
<tr>
<td></td>
<td>File &gt; Open &gt; New Project</td>
</tr>
<tr>
<td><strong>Bold</strong></td>
<td>Displays commands, menu paths, and icon names in procedures:</td>
</tr>
<tr>
<td></td>
<td>Click the File icon and then click Open.</td>
</tr>
<tr>
<td><strong>Times New Roman</strong></td>
<td>Displays an equation:</td>
</tr>
<tr>
<td></td>
<td>2 + 2 = 4</td>
</tr>
<tr>
<td><strong>Text in gray boxes</strong></td>
<td>Describes Cautions or unique functionality of the product.</td>
</tr>
</tbody>
</table>
2. CapSense Technology

Cypress's CapSense controllers use changes in capacitance to detect the presence of a finger on or near a touch surface, as shown in Figure 2-1. This touch button example illustrates a capacitive sensor replacing a mechanical button. The sensing function is achieved using a combination of hardware and firmware. The following section provides an overview of capacitive sensing technology and CapSense solutions.

Figure 2-1. Illustration of a Capacitance Sensor Application

2.1 Capacitive Sensing Methods

Capacitance can be measured between two points using either self-capacitance or mutual-capacitance.

Figure 2-2. Self-Capacitance and Mutual-Capacitance Methods

2.1.1 Self-Capacitance

Self-capacitance uses a single pin and measures the capacitance between that pin and ground. A self-capacitance sensing system operates by driving current on a pin connected to a sensor and measuring the voltage. When a finger is placed on the sensor, it increases the measured capacitance. Self-capacitance sensing is best suited for single-touch sensors, such as buttons and sliders. Cypress's CapSense solutions use self-capacitance sensing. This approach makes efficient use of pins for single touch sensors and sliders.
2.1.2 Mutual-Capacitance

Mutual-capacitance uses a pair of pins and measures the capacitance between those pins. A mutual-capacitance system operates by driving a current on a transmit pin and measuring the charge on a receive pin. When a finger is placed between the transmit and receive pins, it decreases the measured capacitance. The mutual-capacitance effect is best suited to multitouch systems, such as touchscreens and trackpads.

Cypress’s TrueTouch® touchscreen solutions use mutual-capacitance sensing. See TrueTouch Touchscreen Controllers to learn about these products. Cypress also offers trackpad solutions. Contact your local Cypress sales office directly for more information. To find your local sales office, click here.

2.2 Self-Capacitance Equivalent Model

In a CapSense self-capacitance system, the sensor capacitance measured by the controller is called \( C_x \). When a finger is not on the sensor, \( C_x \) equals the parasitic capacitance of the system. This parasitic capacitance, \( C_P \), is a simplification of the distributed capacitance that includes the effects of the sensor pad, the overlay, the trace between the CapSense controller pin and the sensor pad, the vias through the circuit board, and the pin capacitance of the CapSense controller. \( C_P \) is related to the electric field around the sensor pad. Although Figure 2-3 shows field lines only around the sensor pad, the actual electric field is more complicated.

![Figure 2-3. \( C_P \) and Electric Field](image)

When a finger touches the sensor surface, it forms a simple parallel plate capacitor with the sensor pad through the overlay. The result is called finger capacitance, \( C_F \), and is defined by Equation 1. \( C_F \) is a simplification of a distributed capacitance that includes the effects of the human body and the return path to the circuit board ground.

\[
C_F = \frac{\varepsilon_0 \varepsilon_r A}{D}
\]

Equation 1

Where:
- \( \varepsilon_0 \) = Free space permittivity
- \( \varepsilon_r \) = Dielectric constant of overlay
- \( A \) = Area of finger and sensor pad overlap
- \( D \) = Overlay thickness
With a finger on the sensor surface, $C_X$ equals the sum of $C_P$ and $C_F$.

$$C_X = C_P + C_F$$  

Equation 2

2.3 CapSense Sensing Technology

2.3.1 Sensing Methods

There are a number of capacitive sensing methods currently in use across the electronics industry. Some of them include:

- **Charge Transfer**: The change in sensor capacitance introduced by a finger touch results in the change in charge transfer between the sensor capacitor and the reference capacitor. These incremental charge packets are transferred until a reference voltage is achieved on the reference capacitor, which indicates the presence of a touch.

- **Relaxation Oscillator**: A sensor capacitor is used to set its frequency directly. The capacitance introduced by the finger touch is detected on the sensor by tracking the change in the frequency of the oscillator. When there is a finger touch, the sensor capacitor increases and frequency of the oscillator decreases.

- **TX-RX**: A source waveform is driven on the TX end of a mutual capacitance system and senses the response on the RX end. The received signal reflects changes in sensor capacitance.

- **ADC**: A current source generates a linear voltage ramp on a capacitor. This voltage is input to an analog comparator circuit. The comparator’s output is monitored and a counter increments whenever it transitions from high to low.

Cypress’s CapSense devices measure sensor capacitance using either CapSense with Sigma Delta modulator (CSD) or CapSense Successive Approximation (CSA_EM). Both methods are variants of the ADC method.

2.3.2 Capacitance Conversion

The CapSense algorithm converts the sensor capacitance into a digital count, called raw count. The raw count is interpreted as either a TOUCH or NO TOUCH state for the sensor. The numerical value of the raw count is the digital representation of the sensor capacitance, and increases as the capacitance increases. Sensitivity is a measure of how much the output will change for a given change on the input. The sensitivity of the CapSense sensor has units of counts-per-pF.
2.3.3 CapSense with Sigma Delta Modulator (CSD)

Cypress’s CSD method uses a switched capacitor circuit on the front end of the system to convert the sensor capacitance to an equivalent resistor. A Sigma-Delta modulator converts the current measured through the equivalent resistor into a digital count. When a finger is on the sensor, the capacitance increases and the equivalent resistance decreases. This causes an increase in current through the resistor, resulting in an increase in the digital count.

The CSD method requires a single dedicated pin and a single external component, C\textsubscript{MOD}, or two dedicated pins and two external components, C\textsubscript{MOD} and R\textsubscript{B}, depending on what CapSense controller family is selected. Figure 2-6 shows the CSD configuration for the CY8C21x34 CapSense controller family, and the family requires two external components and two dedicated pins.
The CY8C20xx6A/AS/H family of CapSense controllers uses a single external component \( C_{\text{MOD}} \) and a single dedicated pin.

Figure 2-7. CY8C20xx6A/AS/H CSD Block Diagram

2.3.4 CapSense Successive Approximation Electromagnetic Compatible (CSA_EMCC)

Cypress’s CSA_EMCC method also uses a switched-capacitor circuit on the front end of the system to convert the sensor capacitance to an equivalent resistor. An internal constant current source called the iDAC is calibrated with a successive approximation procedure until an equilibrium voltage develops across the integration capacitor \( C_{\text{MOD}} \). This equilibrium voltage is measured using a single slope ADC. When you place a finger on the sensor the capacitance increases. Further, this causes the equilibrium voltage on \( C_{\text{MOD}} \) to decrease and the ADC output increases that result in an increase in the digital count.

The CSA_EMCC method requires a single dedicated pin and a single external component, \( C_{\text{INT}} \), which is an integration capacitor that is used by the single-slope ADC.

The CSA_EMCC CapSense algorithm is developed to work well in the presence of RF interference. CSA_EMCC is used in applications where CapSense is exposed to conducted interference, AC noise, and other noise sources such as inverters, transformers, and power supplies. For more detail on CSA_EMCC CapSense, see Electromagnetic Compatibility (EMC) Considerations.
2.4 CapSense Tuning

Optimal CapSense system performance depends on board layout, button dimensions, overlay material, and application requirements. These factors are discussed in Design Considerations. In addition to these factors, switching frequency and threshold levels must be carefully selected for robust and reliable performance. Tuning is the process of determining the optimum values for these parameters. Tuning is required to maintain high sensitivity to touch and to compensate for process variations in the sensor board, overlay material, and environmental conditions.

Before we move further, a number of terms need to be defined to understand the tuning process.

2.4.1 Definitions

- **Raw Count**: As seen in Figure 2-9, sensor capacitance is converted into a count value by the CapSense algorithm. The unprocessed count value is referred to as raw count. Processing of the raw count results in ON/OFF states for the sensor.

- **Baseline**: The baseline is an estimate of the average sensor count level when the sensor is in the OFF state. The baseline provides a reference level for the ON/OFF comparison.

- **Difference Count**: Subtracting the baseline level from the raw count produces the difference count that is used in the ON/OFF decision process. The actual baseline is dynamically adjusted by the user module to compensate for environmental changes through a process called baseline update.

The thresholds are offset by a constant amount from the baseline level. The thresholds have the following functions:

- **Noise Threshold**: If the difference count is below the noise threshold, then the baseline is updated.

- **ON Threshold (Finger Threshold + Hysteresis)**: If the difference count is increasing and exceeds the level of (Finger Threshold + Hysteresis), then the sensor state changes from OFF to ON.

- **OFF Threshold (Finger Threshold - Hysteresis)**: If the difference count is decreasing and drops below the level of (Finger Threshold - Hysteresis), then the sensor state changes from ON to OFF.
2.4.2 Signal-to-Noise Ratio (SNR)

Signal is a generic engineering term that can have many meanings. For the capacitive sensor application under consideration for CapSense applications, signal is defined as the change in the average sensor output between the OFF and ON states when the rising edge of the difference count starts below the noise threshold.

Noise is another term that has many meanings. The following discussion presents a definition of CapSense noise that uses a simple mathematical model of the sensor output over time.

When the sensor is in the OFF state, the counts, X(t), can be modeled by an average count and a noise component.

\[ X(t) = X_0 + N_0(t) \]  

Equation 3

- \( X_0 \) is the average of \( X(t) \)
- \( N_0(t) \) is the noise component for \( t \) during the OFF state

The same model applies when the sensor is in the ON state.

\[ X(t) = X_1 + N_1(t) \]  

Equation 4

- \( X_1 \) is the average of \( X(t) \)
- \( N_1(t) \) is the noise component for \( t \) during the ON state

\( X_0 \) is called the baseline level of the raw counts. The difference between \( X_0 \) and \( X_1 \) is called the signal, \( S \).

\[ S = (X_1 - X_0) \]  

Equation 5

The noise components \( N_0(t) \) and \( N_1(t) \) are similar but not identical. For example, \( N_1(t) \) usually contains a higher level of AC line noise in finger sensing applications compared to \( N_0(t) \). This occurs because the human body acts as an antenna to 50 Hz and 60 Hz line noise, and the finger contact with the sensor overlay couples the noise into the CapSense system.

We define the noise level \( N \) as the worst case measured peak noise in the OFF state.

\[ N = \text{max}(N_0(t)) = \text{max}(X(t)) - X_0 \]  

Equation 6

Thus, CapSense Signal-to-Noise Ratio, SNR, is defined as the ratio of signal (S) to noise (N).

\[ \text{SNR} = \frac{S}{N} \]  

Equation 7

For robust operation of CapSense, a minimum SNR of 5:1 is recommended.

![Figure 2-9. Signal and Noise](image)

2.4.3 Measuring SNR

SNR should be measured in the noise environment where CapSense is intended to be used. In other words, measure the system SNR under worst-case noise conditions.

The first step in measuring SNR is to monitor the raw count for each sensor. This can be done using data logging to a text file and plotting in a spreadsheet, or through the use of the Cypress MultiChart GUI tool and I2C-USB Bridge (see chapter 7 - Resources for more details). Whatever the method, the raw count should be observed for SNR.
measurement. The difference count should not be used in the measurement of SNR since it is a function of the baseline update process, which involves filtering (filling the "bucket") and nonlinear threshold events.

Another factor to consider is how the signal is produced. The worst-case ON and OFF scenario should be used when measuring SNR. If the system is designed to sense the presence of a finger, then measure SNR with a light touch of the sensor area, and position the contact point slightly off-center. For automated testing, a worst-case finger touch (0.1 pF) can often be simulated by an equivalent metal disc that is the size and shape of a small coin.

As an example of measuring SNR, consider the raw count waveform in Figure 2-9.

\[
X_0 = 5925 \text{ counts} \\
X_1 = 6055 \text{ counts} \\
S = 130 \text{ counts} \\
N = 5940 - 5925 = 15 \text{ counts} \\
\text{SNR} = 130:15 = 8.6:1
\]

2.4.4 SmartSense Auto-Tuning

2.4.4.1 What is SmartSense?

Tuning the touch sensing user interface is a critical step in ensuring proper system operation and a pleasant user experience. The typical design flow involves tuning the sensor interface in the initial design phase, during system integration, and finally production fine-tuning before the production ramp. Because tuning is an iterative process, it can be time-consuming. SmartSense Auto-Tuning helps to simplify the user interface development cycle. Also, the method is easy to use and reduces the design cycle time by eliminating the tuning process throughout the product development cycle, from prototype to mass production.

2.4.4.2 What does SmartSense do?

SmartSense tunes each CapSense sensor automatically at power up and then monitors and maintains optimum sensor performance during runtime. The number of parameters to be tuned is reduced from 17 in CSD to 4 with SmartSense.

- **Power-up tuning**—SmartSense tunes the parameters of each sensor based on the individual sensor parasitic capacitance to get the desired sensitivity for the sensor.
- **Runtime tuning**—Noise in the system is measured dynamically. The thresholds are adjusted accordingly for each sensor to overcome false triggering due to dynamic variations in noise in the CapSense system.

2.4.4.3 How and where is SmartSense helpful?

SmartSense technology adapts for manufacturing variations in PCBs, overlays, and noise generators, such as LCD inverters, AC line noise, and switch-mode power supplies and automatically tunes them out. SmartSense handles changes in system environment, such as temperature, humidity, and noise sources such as RF, SMPS, LCD Inverter, and AC line noise. In systems with special requirements or very high C\(e\) (greater than 45 pF), auto-tuning may not be the ideal solution.

The following sections describe scenarios in which SmartSense is instrumental in adapting to the external noise. By maintaining a robust signal-to-noise ratio, the false triggering of buttons is prevented.

2.4.4.3.1 Different Noise Levels in Different Designs

SmartSense technology dynamically tunes itself (adjusts noise and finger thresholds) for different noise environments. In Figure 2-10, Design A and Design B have different noise levels. To maintain a minimum SNR of 5:1, dynamic threshold adjustment is required. SmartSense does this automatically, which allows seamless transition from one model to another with minimal or no tuning required.
2.4.4.3.2 Noise Spikes during Production

SmartSense technology also automatically tunes out the noise spikes (in production) that may not be seen during the design stage, as indicated in Figure 2-11. Noise spikes is a powerful feature prevents false button presses in the end system, which prevents a failure analysis for a mass production design.

2.4.5 SmartSense EMC (SmartSense Electromagnetic Compatible)

In addition to the SmartSense Auto-Tuning algorithm discussed previously, the SmartSense EMC user module includes a unique algorithm to improve robustness of capacitive sensing algorithm/circuit against high-frequency conducted and radiated noise.

Every electronic device must comply with specific limits for radiated and conducted external noise. These limits are specified by several regulatory bodies (for example, FCC, CE, U/L, and so on). An excellent PCB layout design, power supply design, and system design is mandatory for a product to pass the conducted and radiated noise tests. In some instances, ideal design practices cannot be followed because of the cost and form factor limitations of the
product. SmartSense EMC with superior noise immunity is well suited and useful for such applications to pass radiated and conducted noise tests.

## 2.5 Sensor Types

Capacitive sensors can be broadly classified into four categories: buttons, sliders, touchscreens / trackpad, and proximity sensors. Different sensor types cater to different market segments.

![Figure 2-12. Types of Capacitive Sensors](image)

- **Button (zero-dimensional)**
- **Slider (one-dimensional)**
- **Touchscreen and Trackpad (two-dimensional)**
- **Proximity (three-dimensional)**

### 2.5.1 Buttons (Zero-Dimensional Sensors)

CapSense buttons are used in a wide variety of applications including: home appliances, medical devices, televisions, monitors, audio systems, photo frames, notebooks, home security systems, white goods, industrial products, and lighting controls. To get higher reliability, lower cost, and appealing industrial design, use CapSense buttons instead of mechanical buttons.

#### 2.5.1.1 Simple Buttons

The simplest capacitive sensor consists of a copper pad connected to a CapSense controller pin with a trace. A button is defined as the combination of the copper sensor pad and the nonconductive overlay material. The button is surrounded by grounded copper hatch separated by an annular gap. Each button requires one I/O pin of the CapSense controller.
2.5.1.2 Matrix Buttons

In applications requiring a high number of buttons such as a calculator keypad or a QWERTY keyboard, capacitive sensors can be arranged in a matrix. This allows a design to have more buttons than there are I/O pins on the CapSense controller.

Figure 2-14. Typical Matrix Buttons

A matrix button design consists of two groups of capacitive sensors: Row sensors and Column sensors. When a button is touched, it can be resolved by identifying the row and column sensors that are both in the TOUCH state. The number of buttons supported by the matrix is equal to the product of the number of rows and the number of columns.

Number of Matrix buttons = Number of Row Sensors × Number of Column Sensors  Equation 8

Using a matrix button design can significantly reduce the number of I/O pins required. For example, the matrix in Figure 2-14 implements 12 buttons, but requires only seven I/O pins for sensors. Additional dedicated pins need to be assigned to external components, depending on the sensing method selected.

Matrix buttons can only be sensed one at a time. When more than one row or column sensor is in the TOUCH state, then the finger location cannot be resolved, and the situation is considered an invalid condition. Some applications require multiple buttons to be sensed simultaneously, such as a keyboard with a Shift, Ctrl, and Alt key. In this case, the Shift, Ctrl, and Alt keys should be designed as individual buttons, or should be changed to a mutual-capacitance sensor design.

2.5.2 Sliders (One-Dimensional Sensors)

Sliders are used for controls requiring gradual adjustments. Examples include a lighting control (dimmer), volume control, graphic equalizer, and speed control. A slider is built using an array of capacitive sensors called segments that are placed adjacent to one another. Actuation of one segment results in partial actuation of physically adjacent segments. By using an interpolation method called a centroid, you can achieve a higher resolution than the number of slider segments. In a typical application, a slider with five segments can resolve at least 100 physical finger positions on the slider. High resolution makes for smooth transitions in light or sound as a finger glides across a slider.

2.5.2.1 Linear Sliders

In a linear slider, each CapSense controller I/O pin is connected to one slider segment. A zigzag pattern (double chevron) is recommended for slider segments. This layout ensures that when a segment is touched, the adjacent segments are also partially touched. Sensor data from multiple sensors improves the estimation of finger position. The maximum number of slider segments is a function of the number of available CapSense controller pins and the required response time.
2.5.2.2 Diplexed Sliders

In a diplexed slider, each CapSense controller I/O pin is connected to two different slider segments. This allows a design to have twice as many slider segments as there are I/O pins. For example, a diplexed 16-segment slider requires only eight CapSense controller I/O pins.

For a diplexed slider to work properly, the slider segments must be connected to the CapSense controller I/O pins in a pre-determined order. The first half of the segments are connected to the CapSense controller I/O pins sequentially (0, 1, 2 ... 7) and operate similar to a linear slider. The second half of the segments are connected to the same CapSense controller I/O pins in a non-sequential order. This order exploits the fact that activation of one segment results in partial actuation of neighboring segments. While slider actuation of one half of the slider results in aliasing on to the other half, the levels will be scattered in the untouched half. Sensing algorithms search for strong adjacent segment actuation and ignore scattered actuation to determine finger position on the slider accurately.
2.5.2.3 Radial Sliders
Radial sliders are similar to linear sliders in that finger position is estimated using data from adjacent sensors; however, radial sliders are continuous (does not have a beginning or end).

![Figure 2-17. Radial Slider](image)

Area contacted by finger

2.5.3 Touchscreens and Trackpads (Two-Dimensional Sensors)
Cypress’s TrueTouch touchscreen solutions use mutual capacitance sensing. For more information on these products, see TrueTouch Touchscreen Controllers. Cypress also offers trackpad solutions. Contact your local Cypress sales office directly for more information. To find your local sales office, click here.

2.5.4 Proximity (Three-dimensional Sensors)
Proximity sensors detect the presence of a hand or other conductive object before it makes contact with the touch surface. Imagine a hand stretched out to operate a car audio system in the dark. The proximity sensor causes the buttons of the audio system to glow through backlight LEDs when the user’s hand is near. One implementation of a proximity sensor consists of a long trace on the perimeter of the user interface, as shown in Figure 2-18.

![Figure 2-18. Proximity Sensor](image)

Another way to implement a proximity sensor is by ganging sensors together. This is accomplished by combining multiple sensor pads into one large sensor using firmware (see Firmware Component) to connect the sensors from the internal analog multiplexer bus. Make sure you do not exceed the Cp limit for the sensing method when ganging sensors together.
2.6 Sensor Construction

2.6.1 Field Coupled via Copper Trace (PCB)

Features of a PCB-based design:
- Most common implementation
- Copper pads etched on the surface of the PCB act as sensor pads
- Electric field emanates from the copper sensor pad to ground plane
- No mechanical moving parts
- A nonconductive overlay serves as the touch surface for the button
- Ideal topology for simple flat panel designs
- Low BOM cost

For more information on springs, see Appendix A: Springs.

2.6.2 Field Coupled via Spring/Gasket/Foam
Features of a design based on springs/gaskets/foam:
- Electrical field coupled from PCB to overlay using a compressed spring, or conductive gasket or foam
- Conductive material itself acts as capacitive sensor pad
- No mechanical moving parts. Springs and foam do not move
- Coupled to touch sensor surface through nonconductive overlay
- Any conductive overlay serves as the button touch surface
- Ideal topology for curved, sloping, or otherwise irregular front panels
- Ideal for designs where touch sensor surface is physically separated from silicon or mother board
- Ideal for designs where CapSense and mechanical button combination is desired

For more information on springs, see Appendix A: Springs.

2.6.3 Field Coupled via Printed Ink
Features of a design based on printed ink:
- Electric field coupled with printed patterns on a flexible substrate using conductive ink
- High series resistance due to higher ohms-per-square of printed ink compared to copper
- High parasitic capacitance due to thin PCB
- No mechanical moving parts, but substrate is flexible
- Coupled to touch sensor surface with a nonconductive overlay
- Ideal topology for flexible front panels
- Flexible PCB can be one-layer or two-layer film

2.6.4 Field Coupled via ITO Film on Glass
Features of a design based on ITO film:
- Electric field coupled with printed or deposited patterns on glass
- Higher series resistance of ITO films compared to copper and printed ink
- No mechanical moving parts
- Ideal topology for graphical front panels

2.7 User Interface Feedback
Effective user interface designs include some feedbacks to the user when they are using the capacitive touch sense buttons. There are various forms of feedback, including visual, audio, and haptic (tactile). Depending on the user interface design, multiple types of feedback can be used in combination.

2.7.1 Visual Feedback
LEDs and LCDs provide visual feedback.

2.7.1.1 LED-based Visual Feedback
Visual feedback is widely used in user interfaces. LEDs are used to indicate the status of buttons, sliders, and proximity sensors. LEDs can implement different effects when the sensor status changes.

2.7.1.1.1 LED ON/OFF
In visual feedback’s simplest form, LEDs are turned ON or OFF in response to a finger touch. General-purpose I/Os are used to drive LEDs in either a sourcing or sinking configuration, as shown in Figure 2-21.
2.7.1.1.2 Advanced LED Effects
For user interfaces that require sophisticated visual effects, a single hardware PWM or timer can be used to drive the LEDs. By varying the duty cycle of the PWM output, you can achieve advanced effects, such as variable LED brightness, fading, and breathing. A single hardware PWM or timer can be used to drive multiple LEDs.

2.7.1.1.3 LED Brightness
By varying the duty cycle of the PWM output, you can adjust the LED brightness as shown in Figure 2-22. This enables adjusting your user interface brightness in response to ambient lighting conditions.
2.7.1.1.4  LED Fading
By gradually changing the duty cycle between LED states, you can achieve a fading effect (see Figure 2-23). For example, the LED appears to “fade in” (from OFF to ON) when the duty cycle is increased in a series of small steps.

Figure 2-23. LED Fading

2.7.1.1.5  LED Breathing
Gradually increasing and decreasing the duty cycle between two levels on a continuous basis makes the LED appear to “breathe”, as shown in Figure 2-24. LED breathing is useful when a system is in idle or stand-by mode. For example, a power button can appear to breathe to alert the user that it is active and can be operated.

Figure 2-24. LED Breathing

Human Eye Sensitivity
The human eye’s sensitivity to the brightness of a light source looks similar to a logarithmic function (Figure 2-25). To provide a visible linear brightness change, change the PWM duty cycle in an exponential manner.

Figure 2-25. Human Eye Brightness Perception versus LED Luminous Flux
The linear brightness levels are transferred to exponential duty cycle values using the lookup table. The following expression is used for the conversion:

\[ N_{\text{out}} = A(\exp(N_{\text{in}}, b) - 1) \]  \hspace{1cm} \text{Equation 9}

\[ A = \frac{N_{\text{MAX}}}{\exp(N_{\text{MAX}}, b) - 1} \]  \hspace{1cm} \text{Equation 10}

Figure 2.26 illustrates the table graphs at different values of parameter \( b \). Note that \( N_{\text{MAX}} \) is set to 255. This expression converts an 8-bit unsigned byte value to the same range. The figure shows that the transfer characteristic becomes more exponential as the \( b \) parameter increases.

### 2.7.1.2 LCD-based Visual Feedback

LCDs provide visual feedback for CapSense buttons and sliders. The main advantage of using an LCD is that it can provide more information along with the feedback for each button press event. PSoC has built-in user modules for driving Hitachi HD44780A LCD module. This user module supports high-level and low-level APIs that can directly display data on the screen with ease. Figure 2.27 shows the typical connection for using the Hitachi HD44780A LCD module.

![Figure 2.27. Hitachi Dot Matrix LCD Pin Connections](image)

PSoC can also control LCDs through I^2C. The I2CHW user module available in PSoC controls the GLK 24064-25 WB graphics LCD. Sending the commands through the I^2C bus is simplified. The CSD user module is configured to scan a set of buttons and any button press event initiates an I^2C transfer from the PSoC to the LCD as a feedback. The typical circuit diagram for driving the GLK 24064-25 WB graphics LCD follows.
2.7.2 Haptic Feedback

Haptic, or tactile, feedback uses vibration to let you know that the system has detected a finger touch. Vibrations are created by an actuator (DC motor) with eccentric rotating mass (ERM). By using a PWM and a timer in the CapSense controller, different kinds of tactile feedback can be generated.

Cypress offers two haptics-enabled CapSense controllers. See the haptics datasheet here.

2.7.3 Audible Feedback

Audible feedback for CapSense buttons is implemented using a buzzer. The pulse-width modulator (PWM) is used to output the PWM signal required for driving the buzzer as specified in the buzzer datasheet. The PWM user module available in PSoC is used for this purpose. PSoC can implement CapSense through its CSA and CSD algorithms. The CSD user module is configured to scan a set of buttons and sliders. When a button press event occurs, the feedback is given by driving the buzzer at a particular intensity level. The circuit diagram for implementing the buzzer feedback follows.

Figure 2-29. Implementing Audible Feedback for CapSense in CapSense Plus/CapSense Express Devices

2.7.3.1 CapSense with Audible Feedback Configuration

Select CSD and PWM8 user module from the user module list. Set the parameters for the CSD user module as shown in Table 2-1. Calculate the PWM user module parameters based on the buzzer’s resonant frequency. For example, consider the buzzer CD1206 with a resonant frequency of 2.4 kHz. A 2.4-kHz PWM signal with a 50-percent duty cycle is required to drive the buzzer to produce proper audio feedback.
To calculate the clock dividers to obtain a 2.4-kHz PWM output, see Figure 2-30.
The system clock is set to 24 MHz. The required PWM output frequency is 2.4 kHz. For this reason:

\[
\text{SysCLK} \div (N1 \times N2 \times (\text{PeriodValue} + 1)) = 2.4 \text{ kHz}
\]

Equation 11

Where, N1 and N2 are the VC1 and VC2 clock divider values, respectively. Period Value is the value of period register input to the PWM.

That means:

\[
24 \text{ MHz} / (N1 \times N2 \times (\text{PeriodValue} + 1)) = 2.4 \text{ kHz}
\]

Equation 12

Rearranging the equation gives the following result:

\[
N1 \times N2 \times (\text{PeriodValue} + 1) = 10000
\]

Equation 13

The previous equation has various integral solutions. For simplicity, this example uses N1 = 4 and N2 = 10. Substituting these values in the previous equation generates the values:

\[
\text{PeriodValue} + 1 = 10000(4 \times 10) \equiv 250
\]

Equation 14

Thus, Period Value is 249. To have a 50-percent duty cycle, the Compare value for the PWM is set as:

\[
(\text{PeriodValue} + 1)/2 = (249 + 1)/2 \equiv 125
\]

Equation 15

User module parameters are matched as shown in the following table.

Table 2-1. PWM8 User Module Parameters

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Name</td>
<td>PWM</td>
</tr>
<tr>
<td>Configuration</td>
<td>8 bit</td>
</tr>
<tr>
<td>Clock</td>
<td>VC2</td>
</tr>
<tr>
<td>Period</td>
<td>249</td>
</tr>
<tr>
<td>Pulse Width</td>
<td>125</td>
</tr>
<tr>
<td>Compare Type</td>
<td>Less than</td>
</tr>
<tr>
<td>Interrupt Type</td>
<td>Compare True</td>
</tr>
<tr>
<td>Clock Sync</td>
<td>Sync to Sysclk</td>
</tr>
</tbody>
</table>

Note that the CSD user module automatically varies the clock dividers based on scan speed and resolution settings of the CSD user module. Therefore, re-enter the clock dividers every time the PWM module is invoked by writing the values directly to the configuration register OSC_CR1. For details about the Configuration register OSC_CR1, see the Technical Reference Manual.

The clock dividers VC1, VC2, and VC3 vary with the CSD scan speed and resolution, as shown in Table 2-2 and Table 2-3.
2.8 Water Tolerance

Capacitive touch sensing is effective in applications where the touch sensing zone is exposed to moisture, rain, or water drops. Such applications include automotive applications, outdoor equipment, ATMs, public access systems, and portable devices such as cell phones, PDAs, and kitchen and bathroom applications. Two common triggers are water droplets on sensors and the flow of water on the PCB. Shield electrodes and guard sensors (see Shield Electrode and Guard Sensor for layout) help in achieving a water-tolerant capacitive sensing design. For more information on the working of guard sensors and shield electrodes, see the CY8C21x34 Design Guide.

- **Water droplets:** Use a shield electrode to prevent false triggering of buttons due to water droplets. Device operation is guaranteed (sensors respond to finger touch) in case of water droplets.
- **Water stream:** Use a shield electrode and guard sensor to prevent sensors from being triggered because of water streams. Sensors do not respond to finger touch in the presence of water streams; only false triggering is prevented.

2.9 CapSense System Overview

CapSense touch sensing solutions include the entire system environment in which they operate. This includes:

- Hardware component, such as PCB and guard sensor
- Firmware component to process the sensor data

2.9.1 Hardware Component

The CapSense controller resides within a larger system composed of a specially printed circuit board (PCB), and a touch-surface called the overlay that protects the PCB.
The capacitive sensor pads of a sensor board are formed by the PCB traces. The most common PCB format is a two-layer board with sensor pads and a hatched ground plane on the top, and the electrical components on the bottom. The electrical components include the CapSense controller and associated parts that convert the sensor capacitance into digital counts. Figure 2-31 shows a cross-sectional view of a two-layer board stack-up.

Figure 2-31. Exploded View of the CapSense Hardware

Figure 2-32. Two-Layer Stack-up of a CapSense Board

Four-layer designs are an option when board area must be minimized. PCB layout plays a very important role in CapSense system performance. Best practices are discussed in Design Considerations.

2.9.2 Firmware Component

Firmware is a vital component of the CapSense system that processes the raw count data and makes logical decisions. The amount of firmware development required for your application depends on which CapSense controller family that you select.

Devices from the CapSense Express family are fully configurable either through hardware or through I^2C and do not require any firmware development on the CapSense controller itself. These devices are appropriate for systems where the finger touch data is sent to a host for higher level processing; see Figure 2-33.
Devices from the CapSense and CapSense Plus families are fully programmable. These devices allow complex system level integration. These controllers can process the raw count data as well as perform other system functions. See the CapSense Product Portfolio and CapSense Selector Guide for additional details. Cypress’s PSoC Designer accommodates firmware development in C and assembly language. See Resources for more information on this and other tools.
3. Design Considerations

When designing capacitive touch sense technology into your application, it is important to remember that the CapSense device exists within a larger framework. Careful attention to every level of detail from PCB layout to user interface to end-use operating environment will enable robust and reliable system performance.

3.1 Overlay Selection

In a CapSense design, overlay material is placed over the sensor pad to protect it from the environment and prevent direct finger contact.

3.1.1 Overlay Material

In the Self-Capacitance Equivalent Model section, Equation 1 shows the finger capacitance.

\[ C_F = \frac{\varepsilon_0 \varepsilon_r A}{D} \]

Where:
- \( \varepsilon_0 \) = Free space permittivity
- \( \varepsilon_r \) = Dielectric constant of overlay
- \( A \) = Area of finger and sensor pad overlap
- \( D \) = Overlay thickness

The geometry of a CapSense system is more complex than a parallel plate capacitor. The conductors in the sensor include the finger and PCB copper. However, like a parallel plate capacitor, \( C_F \) is directly proportional to \( \varepsilon_r \). High dielectric constants lead to high sensitivity. Because air has the lowest dielectric constant, any air gaps between the sensor pad and overlay must be eliminated.

Dielectric constants of some common overlay materials are listed in Table 3-1. Materials with dielectrics between 2.0 and 8.0 are well suited to capacitive sensing applications.

<table>
<thead>
<tr>
<th>Material</th>
<th>( \varepsilon_r )</th>
</tr>
</thead>
<tbody>
<tr>
<td>Air</td>
<td>1.0</td>
</tr>
<tr>
<td>Formica</td>
<td>4.6–4.9</td>
</tr>
<tr>
<td>Glass (Standard)</td>
<td>7.6–8.0</td>
</tr>
<tr>
<td>Glass (Ceramic)</td>
<td>6.0</td>
</tr>
<tr>
<td>PET Film (Mylar®)</td>
<td>3.2</td>
</tr>
<tr>
<td>Polycarbonate (Lexan®)</td>
<td>2.9–3.0</td>
</tr>
<tr>
<td>Acrylic (Plexiglass®)</td>
<td>2.8</td>
</tr>
<tr>
<td>ABS</td>
<td>2.4–4.1</td>
</tr>
<tr>
<td>Wood Table and Desktop</td>
<td>1.2–2.5</td>
</tr>
<tr>
<td>Gypsum (Drywall)</td>
<td>2.5–6.0</td>
</tr>
</tbody>
</table>

Conductive material cannot be used as an overlay because it interferes with the electric field pattern. For this reason, do not use paints that contain metal particles in the overlay.
3.1.2 Overlay Thickness

Sensitivity is inversely proportional to overlay thickness, as illustrated in Figure 3-1.

![Figure 3-1. Sensitivity vs. Overlay Thickness](image)

Both signal and noise are affected by the overlay properties. Table 3-2 lists the recommended maximum overlay thicknesses for PSoC CapSense applications with an acrylic overlay material.

**Table 3-2. Maximum Overlay Thickness with an Acrylic Overlay Material**

<table>
<thead>
<tr>
<th>Design Element</th>
<th>Max. Overlay Thickness (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Button</td>
<td>5</td>
</tr>
<tr>
<td>Slider</td>
<td>2</td>
</tr>
<tr>
<td>Touchpad</td>
<td>0.5</td>
</tr>
</tbody>
</table>

3.1.3 Overlay Adhesives

Overlay materials must have good mechanical contact with the sensing PCB. This is achieved using a nonconductive adhesive film. This film increases the sensitivity of the system by eliminating any air gaps between overlay and the sensor pads. 3M™ makes a high performance acrylic adhesive called 200MP that is widely used in CapSense applications in the form of adhesive transfer tape (product numbers 467MP and 468MP).

3.2 ESD Protection

Robust ESD tolerance is a natural byproduct of thoughtful system design. By considering how contact discharge will occur in your end product, particularly in your user interface, it is possible to withstand an 18 kV discharge event without incurring any damage to the CapSense controller.
### Table 3-3. Overlay Material Dielectric Strength

<table>
<thead>
<tr>
<th>Material</th>
<th>Breakdown Voltage (V/mm)</th>
<th>Min. Overlay Thickness at 12 kV (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Air</td>
<td>1200–2800</td>
<td>10</td>
</tr>
<tr>
<td>Wood – dry</td>
<td>3900</td>
<td>3</td>
</tr>
<tr>
<td>Glass – common</td>
<td>7900</td>
<td>1.5</td>
</tr>
<tr>
<td>Glass – Borosilicate (Pyrex®)</td>
<td>13,000</td>
<td>0.9</td>
</tr>
<tr>
<td>PMMA Plastic (Plexiglas®)</td>
<td>13,000</td>
<td>0.9</td>
</tr>
<tr>
<td>ABS</td>
<td>16,000</td>
<td>0.8</td>
</tr>
<tr>
<td>Polycarbonate (Lexan®)</td>
<td>16,000</td>
<td>0.8</td>
</tr>
<tr>
<td>Formica</td>
<td>18,000</td>
<td>0.7</td>
</tr>
<tr>
<td>FR-4</td>
<td>28,000</td>
<td>0.4</td>
</tr>
<tr>
<td>PET Film (Mylar®)</td>
<td>280,000</td>
<td>0.04</td>
</tr>
<tr>
<td>Polymide film (Kapton®)</td>
<td>290,000</td>
<td>0.04</td>
</tr>
</tbody>
</table>

CapSense controller pins can withstand a direct 2-kV event. In most cases, the overlay material provides sufficient ESD protection for the controller pins. Table 3-3 lists the thickness of various overlay materials required to protect the CapSense sensors from a 12-kV discharge as specified in IEC 61000-4-2. If the overlay material does not provide sufficient protection, ESD countermeasures should be applied in the following order: Prevent, Redirect, Clamp.

#### 3.2.1 Preventing ESD Discharge

Preventing the ESD discharge from reaching the CapSense controller is the best countermeasure you can take. Make certain that all paths on the touch surface have a breakdown voltage greater than any voltage to which the surface may be exposed. Also, design your system to maintain an appropriate distance between the CapSense controller and possible sources of ESD. In the example illustrated in Figure 3-2, if L1 and L2 are greater than 10 mm, the system will withstand 12 kV.

![Figure 3-2. ESD Path](image)

If it is not possible to maintain adequate distance, place a protective layer of a high breakdown voltage material between the ESD source and CapSense controller. One layer of 5 mil-thick Kapton® tape will withstand 18 kV. See Table 3-3 for other material dielectric strengths.
3.2.2 Redirect

If your product is densely packed, it may not be possible to prevent the discharge event. In this case, you can protect the CapSense controller by controlling where the discharge occurs. This can be achieved through a combination of PCB layout, mechanical layout of the system, and conductive tape or other shielding material. A standard practice is to place a guard ring on the perimeter of the circuit board. The guard ring should connect to chassis ground.

![Guard Ring Diagram]

As recommended in PCB Layout Guidelines, providing a hatched ground plane around the button or slider sensor can also redirect the ESD event away from the sensor and CapSense controller.

3.2.3 Clamp

Because CapSense sensors are placed in close proximity to the touch surface, it may not be practical to redirect the discharge path. Including series resistors or special purpose ESD protection devices may be appropriate. Adding a series resistor on the vulnerable traces is a cost-effective protection method. This technique works by splitting the dissipation between the resistor and the controller. The recommended series resistance added to the CapSense inputs is 560 ohms. More details are available in the Series Resistor section.

![Series Resistor Diagram]

A more effective method is to provide special purpose ESD protection devices on the vulnerable traces. ESD protection devices for CapSense need to be low capacitance. Table 3-4 lists devices recommended for use with CapSense controllers.

Table 3-4. ESD Protection Devices

<table>
<thead>
<tr>
<th>ESD Protection device</th>
<th>Input Capacitance</th>
<th>Leakage Current</th>
<th>Contact Discharge maximum limit</th>
<th>Air Discharge maximum limit</th>
</tr>
</thead>
<tbody>
<tr>
<td>Manufacturer</td>
<td>Part Number</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Littelfuse</td>
<td>SP723</td>
<td>5 pF</td>
<td>2 nA</td>
<td>8 kV</td>
</tr>
<tr>
<td>Vishay</td>
<td>VBUS05L1-DD1</td>
<td>0.3 pF</td>
<td>0.1 µA &lt;</td>
<td>+/-15 kV</td>
</tr>
<tr>
<td>NXP</td>
<td>NUP1301</td>
<td>0.75 pF</td>
<td>30 nA</td>
<td>8 kV</td>
</tr>
</tbody>
</table>
3.3 Electromagnetic Compatibility (EMC) Considerations

EMC is related to the generation, transmission, and reception of electromagnetic energy that can upset the working of an electronic system. The source (emitter) produces the emission and a transfer or coupling path transfers the emission energy to a receptor, where it is processed, resulting in either desired or undesired behavior. Many electronic devices are required to comply with specific limits for emitted energy and susceptibility to external upsets. Several regulatory bodies worldwide set regional regulations to help ensure that electronic devices do not interfere with each other. These regulations help to prevent your computer from interfering with your television, or worse, a hospital X-ray machine or ventilator, or corrupting the operation of a critical medical monitor.

CMOS analog and digital circuits have very high input impedance. As a result, they are sensitive to external electric fields. Take adequate precautions to ensure their proper operation in the presence of radiated and conducted noise.

3.3.1 Radiated Interference

Radiated electrical energy can influence system measurements and potentially influence the operation of the CapSense processor core. The interference enters the CapSense chip at the PCB level, through the sensor traces, and through other digital and analog inputs.

The following sections discuss layout guidelines to minimize the effects of RF interference.

3.3.1.1 Ground Plane

In general, providing a ground plane on the PCB helps to reduce the RF noise picked up by the CapSense controller.

3.3.1.2 Series Resistor

Every CapSense controller pin has some parasitic capacitance, $C_P$, associated with it. Adding an external resistor forms a low-pass RC filter that can dampen RF noise amplitude.

![Figure 3-5. RC Filter](image)

Place series resistors within 10 mm of the CapSense controller pins.

3.3.1.2.1 CapSense Input Lines

The recommended series resistance for CapSense input lines is 560 ohms. Adding resistance changes the time constant of the switched-capacitor circuit that converts $C_P$ into an equivalent resistor. If the series resistance value is set larger than 560 ohms, the slower time constant of the switching circuit limits the amount of charge that can transfer. This lowers the signal level, which in turn lowers SNR. Smaller values are better, but are less effective at blocking RF.

3.3.1.2.2 Digital Communication Lines

Communication lines, such as I²C and SPI, also benefit from series resistance and 330 ohms is recommended for communication lines. Communication lines have long traces that act as antennae such as the CapSense traces. If more than 330 ohms is placed in series on these lines, the voltage levels fall out of spec with the worst case combination of supply voltages between systems and the input impedance of the receiver.

3.3.1.3 Trace Length

Long traces can pick up more noise than short traces. Long traces also add to $C_P$. Minimize trace length whenever possible.

3.3.1.4 Current Loop Area

Another important layout consideration is to minimize the return path for current. General system emission suppression techniques include adding a decoupling capacitor network and reducing current loops. The current loops create issues for both emission and immunity. A proper ground plane scheme can help reduce the path length. To reduce the impact of parasitic capacitance, give hatched ground instead of solid fill near the sensors or traces. A solid
ground flood is not recommended within 1 cm of CapSense sensors or traces due to an increase in the parasitic capacitance. Figure 3-6 shows an example of an improper grounding scheme. The layout greatly improves by reducing the loop area.

Figure 3-6. Improper Ground Scheme and Ground Loop

In Figure 3-7, two sensors are surrounded by a ground plane that is connected to CapSense controller ground, while a third sensor is surrounded by ground. The third sensor is connected to the other ground plane through the long traces of other circuitry, which creates a large current loop. With this layout, the third sensor may be more susceptible to radiated noise and have increased emissions. These two sections of ground are the same location on the schematic, so they can potentially be one connected area with a better layout.

Figure 3-7. Improper Current Loop Layout

Figure 3-8 illustrates the proper layout for the previous example. The loop area is reduced by connecting the two grounded areas.
3.3.1.5 **RF Source Location**

When systems, such as computer monitors or digital photo frames, are designed with CapSense devices, make sure you prevent noise from LCD inverters and switched-mode power supplies (SMPS) from upsetting the CapSense system. A simple technique to minimize this kind of interaction is to partition the system with noise sources from CapSense inputs, as demonstrated in Figure 3-9. Due to the practical limitations of product size, the noise source and the CapSense circuitry may only be separated by a few inches. This small separation can provide the extra margin required for good sensor performance compared to the case with close proximity between noise source and CapSense.

Figure 3-9. Separating Noise Sources

3.3.2 **Radiated Emissions**

Figure 3-10 shows the impact of rise/fall time of a square wave on the radiated emissions. Note that slowing the transitions introduces the cutoff point and damps the radiated energy level. The internal clock signals of the CapSense controller are slew-controlled to reduce the radiated emission.
The CapSense sensing methods use a switched capacitor front end to interact with the sensors. Selecting a low-frequency for the switched-capacitor clock helps you to reduce the radiated noise from the CapSense sensor.

3.3.3 Conducted Immunity and Emissions
The noise current generated by high-frequency switching circuits entering the system through the power and communication lines is called conducted noise.

3.3.3.1 Board Level Solutions
Proper use of decoupling capacitors as recommended by the datasheet can limit the problem with conducted emissions. For further protection, a passive filter can be used. Passive filter effectively limits not just the conducted noise emitted but also the noise entering the system. Thus, it improves the conducted noise immunity of the system.

A pi-filter is a simple bidirectional low-pass filter. The two main types of pi-filters are the series inductor and the series resistor. The series inductor pi-filter has two shunt capacitors and one series inductor configured similar to the Greek letter π, as shown in Figure 3-11. The noise is filtered by all three elements (L1, C1, and C2) in both directions. The bidirectional nature of the filter is important. Not only does it prevent the supply noise from affecting sensitive parts, it can also prevent the switching noise of the part from coupling back onto the power planes.

3.3.3.2 Power Supply Solutions
The following guidelines help you to prevent conducted noise from entering your CapSense design:

- Provide GND and VDD planes that reduce current loops
- If the CapSense controller PCB is connected to the power supply by a cable, minimize the cable length and consider using a shielded cable
- To reduce high-frequency noise, place a ferrite bead around power supply or communication lines
3.4 Software Filtering

Software filters are one of the techniques of dealing with high levels of system noise. Table 3-5 lists the types of filters that are useful for CapSense.

Table 3-5. CapSense Filter Types 49

<table>
<thead>
<tr>
<th>Type</th>
<th>Description</th>
<th>Application</th>
</tr>
</thead>
<tbody>
<tr>
<td>Average</td>
<td>Finite impulse response filter (no feedback) with equally weighted coefficients</td>
<td>Periodic noise from power supplies</td>
</tr>
<tr>
<td>IIR</td>
<td>Infinite impulse response filter (feedback) with a step response similar to an RC filter</td>
<td>High frequency white noise (1/f noise)</td>
</tr>
<tr>
<td>Median</td>
<td>Nonlinear filter that computes median input value from a buffer of size N</td>
<td>Noise spikes from motors and switching power supplies</td>
</tr>
<tr>
<td>Jitter</td>
<td>Nonlinear filter that limits current input based on previous input</td>
<td>Noise from thick overlay (SNR &lt; 5:1), especially useful for slider centroid data</td>
</tr>
<tr>
<td>Event-Based</td>
<td>Nonlinear filter that causes a predefined response to a pattern observed in the sensor data</td>
<td>Commonly used to block generation or posting of nonexistent events</td>
</tr>
<tr>
<td>Rule-Based</td>
<td>Nonlinear filter that causes a predefined response to a pattern observed in the sensor data</td>
<td>Commonly used during normal operation of the touch surface to respond to special scenarios such as accidental multi-button selection</td>
</tr>
</tbody>
</table>

3.4.1 Average Filter

An average filter is a finite impulse response (FIR) filter with equal-weighted coefficients. The average filters work well with periodic noise that is attenuated by spacing the samples out over one noise cycle. Sample spacing is not critical. For example, power line noise can be anywhere from 50 Hz to 60 Hz. Without adjusting the sampling rate, the average filter works well for 50-Hz and 60-Hz noise. Figure 3-12 shows a sample rate that is synchronized with a simple periodic waveform. There is no feedback path in this filter.

![Figure 3-12. Synchronized Sample Rate](image)

The general equation for an average filter is:

\[ y[i] = \frac{1}{N} \left( x[i] + x[i - 1] + \cdots + x[i - N + 1] \right) \]

Equation 16

Figure 3-13 and Figure 3-14 illustrate the results of using an average filter on real CapSense data using the 16-sample filter equation:

\[ y[i] = \frac{1}{16} \left( x[i] + x[i - 1] + \cdots + x[i - 15] \right) \]

Equation 17
The previous examples are representative of power supply noise. The filter works well in this example because the period of the noise is close to the length of the filter \(N = 16\). For more information about how to implement an average filter, see the code example CSA Software Filters with EzI2Cs Slave on CY8C20xx6.

### 3.4.2 IIR Filter

Infinite impulse response filters (IIR) produce a step response similar to RC filters. IIR filters attenuate high-frequency noise components and pass lower frequency signals, such as finger touch–response waveforms.
The general equation for a first-order IIR filter is:
\[ y[n] = \frac{1}{k} \left( x[n] + (k - 1) \times y[n-1] \right) \]

Equation 18

Figure 3-16 and Figure 3-17 illustrate the results of a first-order IIR filter on real CapSense data using the filter equation with \( k = 16 \):
\[ y[n] = \frac{1}{16} \left( x[n] + (15 \times y[n-1]) \right) \]

Equation 19
For more information about how to implement an IIR filter, see the code example CSA Software Filters with EzI2Cs Slave on CY8C20xx6.

3.4.3 Median Filter

Median filters eliminate noise spikes most commonly associated with motors and switching power supplies. In a median filter, a buffer of size N stores the N most recent samples of the input. The median is then computed using a two-step process. First, the buffer values are sorted from smallest to largest; then, the middle value is selected from the ordered list. The buffer is scanned for the median with each update of the buffer. This is a nonlinear filter. The general equation for a median filter is:

\[ y[i] = \text{median}(x[i], x[i-1], ..., x[i-N+1]) \]  

Equation 20

**Figure 3-18** and **Figure 3-19** show the results of a median filter on real CapSense data using the general filter equation with N = 16.

\[ y[i] = \text{median}(x[i], x[i-1], ..., x[i-15]) \]  

Equation 21

**Figure 3-18. Median Filter Noise Spike**
For more information about how to implement a median filter, see the code example CSA Software Filters with EzI2Cs Slave on CY8C20xx6.

3.4.4 Jitter Filter

3.4.4.1 Jitter Filter for Noisy Slider Data

The centroid function is used to estimate finger position on a slider. When the signal level is low, usually because of thick overlay on the slider, the estimate of finger position will appear to shake and jitter even when the finger is held at a fixed position. This jitter noise can be removed using a jitter filter. To do this, the previous input is stored in a buffer. The current input is compared to the previous output. If the difference is greater than $\pm 1$, the output is changed by $\pm 1$ (matching sign), as shown in Equation 22. This is a nonlinear filter.

\[
\begin{align*}
y[i] &= x[i] - 1, & \text{if } x[i] > y[i - 1] + 1 \\
y[i] &= x[i] + 1, & \text{if } x[i] < y[i - 1] - 1 \\
y[i] &= y[i - 1], & \text{otherwise}
\end{align*}
\]

Equation 22

Figure 3-20 shows the results of applying a jitter filter applied to noisy centroid data.
3.4.4.2 Jitter Filter for Raw Counts

Although the jitter filter is intended for use with noisy slider data, it is also used with noisy buttons. If the change in the current input exceeds a set threshold level, the output is changed to the previous input plus or minus the threshold amount. The output is not changed if the current input changes by less than the threshold amount. The general equation for a jitter filter applied to buttons is:

\[
\begin{align*}
    y[i] &= x[i] - \text{threshold}, & \text{if } x[i] > y[i - 1] + \text{threshold} \\
    y[i] &= x[i] + \text{threshold}, & \text{if } x[i] < y[i - 1] - \text{threshold} \\
    y[i] &= y[i - 1], & \text{otherwise}
\end{align*}
\]

Equation 23

Figure 3-21 and Figure 3-22 shows the result of using a jitter filter on real button data with a large component of periodic noise.
3.4.5 Event-Based Filters

Event-based filters involve a special filtering method where a pattern observed in the sensor data causes a predefined response in the CapSense system. The pattern in the data is triggered by an event, such as a handheld product being placed into a pocket, or \( V_{DD} \) dropping suddenly in a camera phone when the camera flash circuit is being charged. One common response used with event-based filter is to block CapSense data transmission until the pattern returns to normal. Another common response is to reset the level of the Baseline reference, defined in Signal-to-Noise Ratio (SNR).

3.4.6 Rule-Based Filters

Rule-based filters are another special filtering method where a pattern observed in the sensor data causes a rule-based response in the CapSense system. Unlike the event-based filter, the rule-based filter acts on patterns in the sensor data that are encountered during normal operation of the touch surface. The rule-based filter takes into account special scenarios on how sensors are used. For example, with a set of radio channel selection buttons, two buttons can be pressed accidentally, but only one should be selected. The rule-based filter sorts out this kind of situation in a predefined way.

3.5 Power Consumption

Minimizing power consumption is an important design goal. For many CapSense systems, extending battery life is critical to the success of the product. In systems that do not use batteries, power consumption still plays a role in optimizing power supply designs to reduce costs and PCB area.

3.5.1 Active and Sleep Current

Active current is the current consumed by the device when all selected analog and digital blocks are enabled and the CPU is running. In typical applications, the CapSense controller does not need to be in the active state all the time. The device can be put into the sleep state to stop the CPU and the major blocks of the device. Current consumed by the device in sleep state is called sleep current. Sleep current is much lower than the active current.

3.5.2 Average Current

In typical applications, sleep state can be invoked periodically to reduce power consumption. This means that during a preset time period, the CapSense controller wakes up from sleep state, performs all necessary operations in the
active state (scan all sensors, update all baselines, check if any sensor is in the TOUCH state, and so on), and then returns to sleep state. The resulting instantaneous current graph is shown in Figure 3-23.

Figure 3-23. Instantaneous Current

Where:
$I(t)$ = Instantaneous current
$I_{Act}$ = Active current
$I_{Slp}$ = Sleep current
$t_{Act}$ = Active time
$t_{Slp}$ = Sleep time
$T$ = Time period of a cycle

The average current consumed by the device over a long period can be calculated by using the following equation.

$$I_{AVE} = \frac{(I_{Act} \times t_{Act}) + (I_{Slp} \times t_{Slp})}{T}$$  \hspace{1cm} \text{Equation 24}

The average power consumed by the device can be calculated as follows:

$$P_{AVE} = V_{DD} \times I_{AVE}$$  \hspace{1cm} \text{Equation 25}

3.5.3 Response Time versus Power Consumption

As illustrated in Equation 25, the average power consumption can be reduced by decreasing $I_{AVE}$ or $V_{DD}$. $I_{AVE}$ may be decreased by increasing sleep time. Increasing sleep time to a very high value leads to poor response time of the CapSense button. Because of this tradeoff between response time and power consumption, the application developer must carefully select the sleep time based on system requirements.

In any application, if both power consumption and response time are important parameters to be considered, then, an optimized method can be used that incorporates both continuous-scan and sleep-scan modes. In this method, the device spends most of its time in sleep-scan mode where it scans the sensors and goes to sleep periodically as explained in the previous section and thereby consuming less power. When you touch a sensor to operate the system, the device jumps to continuous-scan mode where the sensors are scanned continuously without invoking sleep and thereby giving very good response time. The device remains in continuous-scan mode for a specified time-out period. If you do not operate any sensor within this time-out period, the device returns to the sleep-scan mode.

3.6 Pin Assignments

An effective method to reduce interaction between CapSense sensor traces and communication and non-CapSense traces is to isolate each by port assignment. Figure 3-24 shows a basic version of this isolation for a 32-pin QFN package. Because each function is isolated, the CapSense controller is oriented such that there is no crossing of communication, LED, and sensing traces.
The CapSense controller architecture imposes a restriction on current budget for even and odd port pin numbers. For a CapSense controller, if the current budget of an odd port pin is 100 mA, the total current drawn through all odd port pins should not exceed 100 mA. In addition to the total current budget limitation, there is also a maximum current limitation for each port pin. See the datasheet of the CapSense controller used in the application to know the specification of that particular CapSense controller.

All CapSense controllers provide high current sink and source capable port pins. When using high current sink or source from port pins, select the ports that are closest to the device ground pin to minimize the noise.

The following three examples demonstrate common pin assignment mistakes. In Figure 3-25, CapSense and non-CapSense traces are not isolated, and CapSense pins are far from ground. This is an example of a bad pin assignment.

Figure 3-25. Not Recommended - CapSense and non-CapSense Pins in Proximity
The example in Figure 3-26 achieves good isolation, but it has a bad pin assignment because the LEDs are placed next to the ground pin. The CapSense sensors are assigned to the side of the chip that does not include ground. If the CapSense pins are away from the ground pin, the impedance of the ground path increases, which in turn causes the drive circuit’s reference voltage to shift. This shift may lead to false triggering of sensors. For this reason, it is recommended to have CapSense pins near the ground pin.

Figure 3-26. Not Recommended - LEDs and Ground Pins in Proximity

Further, LEDs should not be placed close to $\text{CMOD}/R_B$ pin to avoid crosstalk as illustrated in Figure 3-26.

Figure 3-27. Not Recommended: $\text{CMOD}/R_B$ and LED Pins in Proximity

Note that using the P1.0 and P1.1 pins for LEDs or for communication purposes is not recommended. This is because upon power up, there will be a low pulse on the P1.0 and P1.1 pins. For further clarity you can also refer to the individual device design guides webpage having the sample schematics of all the CapSense devices:

- CY8C21x34 design guide
- CY8C20x34 design guide
- CY8C20xx6A design guide
3.7 PCB Layout Guidelines

In the typical CapSense application, the capacitive sensors are formed by the traces of a printed circuit board (PCB) or flex circuit. Following CapSense layout best practices will help your design achieve higher noise immunity, lower $C_P$, and higher signal-to-noise ratio (SNR). The CapSense signal drops off at high $C_P$ levels due to drive limits of the internal current sources that are part of the CapSense circuitry. The long time constants associated with high $C_P$ are another reason to avoid high $C_P$.

3.7.1 Parasitic Capacitance, $C_P$

The main components of $C_P$ are trace capacitance and sensor capacitance. $C_P$ is a nonlinear function of sensor diameter, trace length, trace width, and the annular gap. There is no simple relation between $C_P$ and PCB layout features, but here are the general trends. An increase in sensor size, an increase in trace length and width, and a decrease in the annular gap all cause an increase in $C_P$. One way to reduce $C_P$ is to increase the clearance between the sensor and ground. Unfortunately, widening the gap between sensor and ground will decrease noise immunity.

3.7.2 Board Layers

Most applications use a two-layer board with sensor pads and a hatched ground plane on the top side and all other components on the bottom side. The two-layer stackup is shown in Figure 3-28. In applications where board space is limited or the CapSense circuit is part of a PCB design containing complex circuitry, four-layer PCBs are used.

![Figure 3-28. Two-Layer Stackup for CapSense Boards](image)

3.7.3 Board Thickness

FR4-based PCB designs perform well with board thicknesses ranging from 0.020 inches (0.5 mm) to 0.063 inches (1.6 mm).

Flex circuits work well with CapSense, and are recommended for curved surfaces. All guidelines presented for PCBs also apply to flex. Ideally, flex circuits should be no thinner than 0.01 inches (0.25 mm). The high breakdown voltage of the Kapton® material (290 kV/mm) used for flex circuits provides built in ESD protection for the CapSense sensors.

3.7.4 Button Design

The best shape for buttons is round. Rectangular shapes with rounded corners are also acceptable. Because sharp points concentrate fields, avoid sharp corners (less than $90^\circ$) when designing your sensor pad.

![Figure 3-29. Recommended Button Shapes](image)
Button diameter can range from 5 mm to 15 mm, with 10 mm being suitable for the majority of applications. A larger diameter helps with thicker overlays.

Annular gap size should be equal to the overlay thickness, but no smaller than 0.5 mm, and no larger than 2 mm. For example, a PCB layout for a system with a 1-mm overlay should have a 1-mm annular gap, while a 3-mm overlay design should have a 2-mm annular gap. The spacing between the two adjacent buttons should be large enough that if one button is pressed, a finger should not reach the annular gap of the other button.

3.7.5 Slider Design

A typical slider pattern is shown in Figure 3-30. The recommended dimensions for slider design are in Table 3-6.

![Figure 3-30. Typical Slider Pattern](image)

Table 3-6. Slider Dimensions

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Min</th>
<th>Max</th>
<th>Recommended</th>
</tr>
</thead>
<tbody>
<tr>
<td>Width of the Segment (A)</td>
<td>1.5</td>
<td>4</td>
<td>Equal to overlay thickness, but within the min/max limits.</td>
</tr>
<tr>
<td>Clearance between Segments (B)</td>
<td>0.5</td>
<td>2</td>
<td>Equal to sensor to ground clearance</td>
</tr>
<tr>
<td>Height of the segment (C)</td>
<td>7</td>
<td>15</td>
<td>12</td>
</tr>
</tbody>
</table>

When any segment is scanned, the adjacent segments are grounded. To maintain uniformity, the two segments on both ends of a slider should also be grounded. Therefore, for a design with a slider of ‘n’ segments, there must be actually n+2 segments. If the hatch around the slider is shielded instead of ground to achieve water tolerance, then the last two segments should also be shielded. For more information, see Shield Electrode and Guard Sensor.

3.7.6 Sensor and Device Placement

- Minimize the trace length from the CapSense controller pins to the sensor pad to optimize signal strength.
- Mount series resistors within 10 mm of the controller pins to reduce RF interference and provide ESD protection.
- Mount the controller and all other components on the bottom layer of the PCB.
- Isolate switching signals, such as PWM, I²C communication lines, and LEDs, from the sensor and the sensor PCB traces. Do this by placing them at least 4 mm apart and fill a hatched ground between CapSense traces and non-CapSense traces to avoid crosstalk.
- Avoid connectors between the sensor and the controller pins because connectors increase Cₚ and decrease noise immunity.

3.7.7 Trace Length and Width

Minimize the parasitic capacitance of the traces and sensor pad. Trace capacitance is minimized when they are short and narrow.

- The maximum recommended trace length is 12 inches (300 mm) for a standard PCB and 2 inches (50 mm) for flex circuits.
- Trace width should not be greater than 7 mil (0.18 mm). CapSense traces should be surrounded by hatched ground with trace-to-ground clearance of 10 mil to 20 mil (0.25 mm to 0.51 mm).
3.7.8 Trace Routing

Route sensor traces on the bottom layer of the PCB, so that the only user interaction with the CapSense sensors is with the active sensing area. Do not route traces directly under any sensor pad unless the trace is connected to that sensor.

Do not run capacitive sensing traces in close proximity to communication lines, such as I²C or SPI masters. If it is necessary to cross communication lines with sensor pins, make sure the intersection is at right angles, as illustrated in Figure 3-31.

Figure 3-31. Routing of Sensing and Communication Lines

3.7.9 Crosstalk Solutions

A common backlighting technique for panels is to mount an LED under the sensor pad so that it shines through a hole in the middle of the sensor. When the LED is switched on or off, the voltage transitions on the trace that drives the LED can couple into the capacitive sensor input, creating noisy sensor data. This coupling is referred to as crosstalk. To prevent crosstalk, isolate CapSense and non-CapSense traces from one another. In the case of CY8C21x34/B, the crosstalk can also occur due to the coupling of the LED voltage transitions with the R_b resistor. To avoid this, isolate the R_b trace from the non-CapSense traces. A minimum separation of 4 mm is recommended. A hatched ground plane also can be placed between those traces to isolate them. LED drive traces and CapSense traces (including R_b trace) should not be routed together.

Figure 3-32. Not Recommended - LED and CapSense in Close Proximity

Figure 3-33. Recommended - LED and CapSense with Wide Separation
Another approach to reducing crosstalk is to slow down the rising and falling edges of the LED drive voltage using a filter capacitor. Figure 3-34 shows an example circuit of this solution. The value of the added capacitor depends on the drive current requirements of the LED; however, a value of 0.1 μF is typical.

Figure 3-34. Filter Capacitor Solution for Crosstalk

3.7.10 Vias
Use the minimum number of vias to route CapSense inputs to minimize parasitic capacitance. The vias should be placed to minimize the trace length, which is usually on the edge of the sensor pad, as shown in Figure 3-35.

Figure 3-35. Vias Placement on Sensor Pad

3.7.11 Ground Plane
Ground fill is added to both the top and bottom of the sensing board. When ground fill is added near a CapSense sensor pad, there is a tradeoff between maintaining a high level of CapSense signal and increasing the noise immunity of the system. Typical hatching for the ground fill is 25 percent on the top layer (7 mil line, 45 mil spacing) and 17 percent on the bottom layer (7 mil line, 70 mil spacing).

Figure 3-36. Recommended Button and Slider Layout Top Layer
3.7.12 Shield Electrode and Guard Sensor

**Shield:**
- Shield-electrode copper-hatch recommendations
  - Top layer – 7-mil trace and 45-mil grid (15 percent fill)
  - Bottom layer – 7-mil trace and 70-mil grid (10 percent fill)
  - Only areas surrounding sensor pads and the CapSense controller should be grounded
- Reduce the size of the shield patterns
  - The shield electrode pattern should surround the sensor pad and exposed traces, and spread no further than 1 cm from these features. Spreading the shield electrode beyond 1 cm has negligible effect on system performance. If board space is limited, the shield can spread less than 1 cm. In Figure 3-38, Sensor-1 shows an example of a shield pattern surrounding a sensor pad and trace routed on the top layer. Sensor-2 shows an example of a shield pattern with a sensor pad without a trace on the top layer.

![Figure 3-38. Shield Electrode Pattern](image)
- Slow the edges of the shield waveform
  - Reduce the slew rate of shield electrode by adding a filter capacitor between the shield electrode port pin and ground.

**Guard Sensor:**
The guard sensor is a copper trace that surrounds all of the buttons, as shown in Figure 3-39.

Figure 3-39. PCB Layout with Shield Electrode and Guard Sensor

- The shield electrode pattern should surround the guard sensor pad and exposed traces, and spread no further than 1 cm from these features.
- The recommended shape for a guard sensor is rectangular with curved edges.
- Recommended thickness of copper trace is 2 mm and distance of Copper trace to shield hash is 1 mm. This is a general recommendation and will vary based on the design.

You can also refer to the individual device design guides webpage for sample layouts of all the CapSense devices:

- CY8C21x34 design guide
- CY8C20x34 design guide
- CY8C20xx6A design guide

### 3.7.13 CapSense System Design with Single Layer PCB

Electronic product manufacturers face constant pressure to lower system costs. Several markets, including consumer and home appliances, are switching to single layer PCBs to support their required product margins. Cypress’s CapSense controllers provide robust touch sensing on single layer PCBs, and their driven shield capability enables longer trace length, proximity sensing, and water tolerance. CapSense delivers IEC (IEC 61000-6-1, IEC 61000-6-2) noise compliant performance for accurate touch responses even in noisy environments using sophisticated firmware algorithms. For more details on implementing CapSense touch sensing on single layer boards, contact capsense@cypress.com.
4. CapSense Product Portfolio

Cypress’s CapSense controller solutions are based on our Programmable System-on-Chip (PSoC®) platform and offer a wide range of features.

4.1 Cypress’s CapSense Controller Solutions

Cypress is the world leader in capacitive sensing technologies and their broad range of solutions provides robust noise immunity, enable quick time to market, and system scalability. With CapSense controllers, you can:

- Replace mechanical components with simple CapSense buttons and sliders.
- Reduce total BOM cost and form factor by integrating CapSense with other system components.
- Optimize board space with our small form factor packaging (WLCSP, SOIC, and QFN).
- Use our advanced sensing techniques for easy finger detection through 15 mm of glass or 5 mm of plastic.
- Get to market more quickly using SmartSense Auto-Tuning.
- Implement additional user interface functionality, such as LED effects, proximity and water rejection.

Cypress offers a wide range of configurable and programmable CapSense controllers. Configurable CapSense controllers are hardware or I2C configurable. Programmable devices provide complete flexibility to meet your exact design requirements, including reducing BOM cost by integrating further system functionality. The different CapSense device families are listed in the following sections.

4.1.1 CapSense Express Controllers (Configurable Solutions)

4.1.1.1 CY8CMBR20xx
The CY8CMBR20xx device features a wide operating voltage range, 1.71-V to 5.5-V operating voltage and CSD capacitive sensing with SmartSense Auto-Tuning. These devices are hardware configurable; no I2C is required.

4.1.1.2 CY8C201xx
CY8C201xx devices feature configurable I/Os that can be used as capacitive sensing inputs or as GPIOs for driving LEDs, interrupt outputs, wake-up on interrupt inputs and other digital I/O functionalities. These devices support register-based configuration through an I2C interface.

4.1.2 CapSense Controllers (Programmable Solutions)

4.1.2.1 CY8C20x34, CapSense
CY8C20x34 devices feature CSA_EMC capacitive sensing and can implement up to 25 CapSense buttons.

4.1.2.2 CY8C20x36A, CapSense
CY8C20xx36A devices can operate at voltages as low as 1.71 V to 5 V; offer both CSA_EMC and CSD capacitive sensing, SmartSense, and support up to 33 CapSense buttons.
4.1.3 CapSense Plus (Programmable Solutions)

CapSense Plus devices feature capacitive touch sensing and additional system functionality. Using CapSense Plus devices can result in significant cost savings. Additional features include:

- Feedback - LED, audio, haptics
- Communication - I²C, TX8, UART, SPI, USB
- Digital functions - PWM, counters, timers
- Analog functions - ADC, comparator
- Bootloaders

4.1.3.1 CY8C20xx6A/AS, CapSense Plus

CY8C20xx6A/AS devices can operate at voltages as low as 1.71 V to 5 V, offer both CSA_EMC, CSD capacitive sensing, SmartSense and SmartSense_EMC, and support up to 33 CapSense buttons, USB, I²C, SPI, and up to 32-KB Flash memory.

4.1.3.2 CY8C21x34/B, CapSense Plus

CY8C21x34 devices feature CSD capacitive sensing and SmartSense sensing (CY8C21x34B) with advanced digital and analog peripherals, are water tolerant, and can support up to 24 CapSense buttons.

4.1.3.3 CY8C24x94, CapSense Plus

CY8C24x94 devices feature CSD capacitive sensing, proximity sensing, are water tolerant, support a wide range of interfaces (SPI, I²C USB 2.0, and UART), and can support up to 44 CapSense buttons.

4.1.3.4 Dynamic Reconfiguration

Dynamic reconfiguration is a clever way to optimize total system cost. There are situations in which the number of digital and analog blocks required by a particular application exceeds the resources of the chip. In these situations, it may be possible to time-share the analog and digital blocks. The process of reusing analog and digital resources at different points in time is called dynamic reconfiguration. If the application requires CapSense, an ADC, and a counter, but not all at the same time, reconfiguring the hardware blocks dynamically will enable all features to be implemented. For more information about dynamic reconfiguration, see the Cypress application note AN2104 – PSoC 1 - Dynamic Reconfiguration with PSoC Designer.
5. CapSense Selector Guide

5.1 Selecting the Right CapSense Device

Several key system requirements must be considered when selecting the best CapSense device for your application.

- Flash and RAM requirements
- Operating voltage range
- Configurable/Programmable
- Number and type of capacitive sensors
- Tuning method
- Water tolerance
- Feedback
- Package size and pin count
- Additional features, such as ADC, communication protocol, and timers

Figure 5-1 provides a high-level guide to the CapSense devices based on design requirements. Table 5-1 identifies the key features for each CapSense product family. After completing the selection process, the next step in your development cycle is to consult the more detailed design guide for the selected device. See the Device Specific Design Guides.
Figure 5-1. Device Selection Tree

CapSense Family Portfolio
CY8CMBR2044, CY8C201xx, CY8C20x34, CY8C20xx6x, CY8C21x34/B, CY8C24x94

- Low Power Application
  - CY8C20xx6A
  - CY8CMBR20xx
  - Configurable
    - CY8CMBR2044
    - CY8CMBR2016
    - CY8C201xx
    - Programmable
    - CY8C20xx6A
- Auto Tuning
  - CY8C21x34B
  - 24 Sensors/GPIOs
  - 4 Sensors & 4 GPIOs
    - CY8C21x34B
    - CY8CMBR2044
- Configurable
  - CY8C21x34/B
  - 24 Sensors/GPIOs
    - CY8C21x34/B
    - 44 Sensors/52 GPIOs
    - CY8C24x94
- Proximity
  - 5 cm Range
    - CY8C21x34/B
    - CY820x34
    - CY8C20xx6A
    - CY8C24x94
  - 2 cm Range
    - CY8C21x34/B
    - 24 Sensors/GPIOs
      - CY8C21x34/B
      - 25 Sensors/GPIOs
        - CY8C20x34
      - 33 Sensors/GPIOs
        - CY8C20xx6A
      - 44 Sensors/52 GPIOs
        - CY8C24x94
- Water Tolerance
  - CY8C21x34/B
  - 24 Sensors/GPIOs
    - CY8C21x34/B
    - 44 Sensors/52 GPIOs
    - CY8C24x94
- Haptics
  - CY8C20xx6H

SmartSense EMC for superior noise immunity
16 Sensors & GPIOs
10 Sensors/GPIOs
24 Sensors/GPIOs
44 Sensors/52 GPIOs
5 cm Range
2 cm Range
Design Requirements
## Table 5-1. CapSense Sensing Method by Product Family

<table>
<thead>
<tr>
<th>CapSense User Modules</th>
<th>CY8CMBR2044</th>
<th>CY8C201xx</th>
<th>CY8C21x34/B</th>
<th>CY8C20x34</th>
<th>CY8C20xx6A/H</th>
<th>CY8C20xx6AS</th>
<th>CY8C24x94</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSD</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>CSA_EMC</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>SmartSense</td>
<td>Yes</td>
<td>Yes (B)</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>SmartSense_EMC</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Yes</td>
</tr>
</tbody>
</table>

## Table 5-2. CapSense Key Features by Product Family

<table>
<thead>
<tr>
<th>Feature</th>
<th>CY8CMBR2044</th>
<th>CY8C201xx</th>
<th>CY8C21x34/B</th>
<th>CY8C20x34</th>
<th>CY8C20xx6A/H</th>
<th>CY8C20xx6AS</th>
<th>CY8C24x94</th>
</tr>
</thead>
<tbody>
<tr>
<td>RAM (Bytes)</td>
<td>512</td>
<td>512</td>
<td>1 K/2 K</td>
<td>1 K/2 K</td>
<td>1 K</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Flash (Bytes)</td>
<td>8 K</td>
<td>8 K</td>
<td>8 K/16 K/32 K</td>
<td>16 K/32 K</td>
<td>16 K</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Operating Voltage</td>
<td>1.71 V–5.5 V</td>
<td>2.4 V–5.25 V</td>
<td>2.4 V–5.25 V</td>
<td>2.4 V–5.25 V</td>
<td>1.71 V–5.5 V</td>
<td>1.71 V–5.5 V</td>
<td>3.0 V–5.25 V</td>
</tr>
<tr>
<td>Configurable</td>
<td>Yes</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Programmable</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Maximum Number of Sensors</td>
<td>4</td>
<td>10</td>
<td>24</td>
<td>25</td>
<td>33</td>
<td>33</td>
<td>44</td>
</tr>
<tr>
<td>Buttons</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Sliders</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Proximity</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>I²C</td>
<td>HW Slave</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td></td>
</tr>
<tr>
<td>SPI</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td>Master and Slave Interface</td>
<td></td>
</tr>
<tr>
<td>UART</td>
<td>UART</td>
<td>Transmitter - Software</td>
<td>Transmitter - Software</td>
<td>Transmitter - Software</td>
<td>UART</td>
<td></td>
<td></td>
</tr>
<tr>
<td>USB</td>
<td></td>
<td>Full speed USB</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Timer</td>
<td>8- to 32-bit timer/counter</td>
<td>13-bit timer</td>
<td>16-bit timer</td>
<td>16-bit timer</td>
<td>8- to 32-bit timer/counter</td>
<td></td>
<td></td>
</tr>
<tr>
<td>PWM</td>
<td>8- to 32-bit deadband option</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>8- to 32-bit</td>
</tr>
<tr>
<td>LED</td>
<td>7-segment support</td>
<td>7-segment support</td>
<td></td>
<td></td>
<td></td>
<td>7-segment support</td>
<td></td>
</tr>
<tr>
<td>LCD</td>
<td>20x2 controller interface</td>
<td>20x2 controller interface</td>
<td>20x2 controller interface</td>
<td>20x2 controller interface</td>
<td>20x2 controller interface</td>
<td></td>
<td></td>
</tr>
<tr>
<td>EEPROM</td>
<td>Emulation</td>
<td>Emulation</td>
<td>Emulation</td>
<td>Emulation</td>
<td>Emulation</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Water</td>
<td>Tolerant</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Tolerant</td>
</tr>
<tr>
<td>Bootloader</td>
<td>I²C</td>
<td>I²C</td>
<td>I²C</td>
<td>I²C</td>
<td>I²C</td>
<td>I²C</td>
<td>I²C</td>
</tr>
<tr>
<td>Parameter</td>
<td>CSD/SmartSense/SmartSense_EMC</td>
<td>CSA_EMC</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>-----------------------------------</td>
<td>-------------------------------</td>
<td>---------</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>External Components</td>
<td>1-2</td>
<td>0-1</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Sensitivity</td>
<td>+++</td>
<td>++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Noise Immunity[^1]</td>
<td>+++</td>
<td>++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Spread Spectrum Precharge Clock</td>
<td>Yes</td>
<td>No</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Scanning Time Range</td>
<td>75 µs...23 ms</td>
<td>90 µs...1 ms</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Water Proof Operation</td>
<td>++</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Shield Electrode</td>
<td>+++</td>
<td>-</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Buttons, Sliders, Touchpad’s</td>
<td>+++</td>
<td>+++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Proximity</td>
<td>+++</td>
<td>++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Linear Transfer Characteristic</td>
<td>Yes</td>
<td>No</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Power Consumption</td>
<td>++</td>
<td>+++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Sleep Support</td>
<td>+++</td>
<td>+++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Environment Changes Immunity</td>
<td>+++</td>
<td>++</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

[^1] CSD, SmartSense, SmartSense_EMC have better immunity to external noise than CSA_EMC. However, CSA_EMC, immunity against noise can be improved by selecting more number of scan frequencies.
6. CapSense Migration Paths

As a system evolves, it may require a CapSense controller with more advanced features. The following sections describe possible migrations among CapSense controllers.

6.1 CY8C20x34 to CY8C20xx6A/H/AS

CY8C20xx6A/H/AS devices implement CSA_EMC using dedicated hardware similar to that in CY8C20x34. Therefore, upgrading your design from CY8C20x34 to CY8C20xx6A/AS do not require any external hardware or firmware changes, simply clone your PSoC Designer project to CY8C20xx6A/AS.

Advantages of CY8C20xx6A/H/AS over CY8C20x34:
- More I/O pins
- Wider operating voltage range
- Lower power consumption
- Immersion TS2000 Haptics Technology for ERM control

6.2 CY8C21x34/B / CY8C24x94 to CY8C20xx6A/H/AS

All three devices feature CSD capacitive sensing. CY8C20xx6A/H/AS devices implement CSD using dedicated hardware as opposed to programmable digital and analog blocks as in CY8C21x34/B and CY8C24x94 devices. Furthermore, CY8C21x34/B and CY8C24x94 devices use an external bleed resistor in the current measuring circuit, whereas CY8C20xx6A/H/AS devices use an on-chip iDAC. These architectural differences necessitate different external circuitry, user module parameters, and settings for similarly named parameters and APIs between the devices.

Advantages of CY8C20xx6A/H/AS over CY8C21x34/B and CY8C24x94:
- Eliminates an external hardware component
- Wider operating voltage range
- Lower power consumption
- Immersion TS2000 Haptics Technology for ERM control

6.3 CY8C20xx6A/H/AS to CY8C21x34/B / CY8C24x94

All three devices feature CSD capacitive sensing. CY8C20xx6A/H/AS devices implement CSD using dedicated hardware as opposed to programmable digital and analog blocks as in CY8C21x34/B and CY8C24x94 devices. Furthermore, CY8C21x34/B and CY8C24x94 devices use an external bleed resistor in the current measuring circuit, whereas CY8C20xx6A/H/AS devices use an on-chip iDAC. These architectural differences necessitate different external circuitry, user module parameters, and settings for similarly named parameters and APIs between the two devices. If the USB module is used in CY8C20xx6A, then migration is not possible to CY8C21x34/B.

Advantages of CY8C21x34/B and CY8C24x94 over CY8C20xx6A/H/AS:
- Proximity up to 5 cm
- Water tolerance
6.4 Pin-to-Pin Compatibility

When migrating from one device to another, PCB change is not required if you assign the same pin functionality to the new device. If this is the case, then the two devices are pin-to-pin compatible. Table 6-1 gives information about available device packages and pin-to-pin compatibility between the same packages of CY8C20x34, CY8C21x34/B, CY8C20xx6A/AS, and CY8C24x94 devices.

Table 6-1. Pin-to-Pin Compatibility

<table>
<thead>
<tr>
<th>Package</th>
<th>CY8C20x34</th>
<th>CY8C21x34/B</th>
<th>CY8C20xx6A/AS</th>
<th>CY8C24x94</th>
</tr>
</thead>
<tbody>
<tr>
<td>16 QFN</td>
<td>N(^a)</td>
<td>-</td>
<td>N(^a)</td>
<td>-</td>
</tr>
<tr>
<td>16 SOIC</td>
<td>N</td>
<td>N</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>24 QFN without USB</td>
<td>Y</td>
<td>-</td>
<td>Y</td>
<td>-</td>
</tr>
<tr>
<td>28 SSOP</td>
<td>Y</td>
<td>Y</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>30 WLCSP</td>
<td>N</td>
<td>-</td>
<td>N</td>
<td>-</td>
</tr>
<tr>
<td>32 QFN without SMP</td>
<td>Y(^b)</td>
<td>Y</td>
<td>Y(^e)</td>
<td>-</td>
</tr>
</tbody>
</table>

a. Pin 2 is different: CY8C20x34 → P2[1], CY8C21x34/B → P2[3]. If Pin 2 is used, modify the firmware for migration.

b. Without USB

Symbols:
Y   pin-to-pin compatible (same package dimensions)
N   packages not pin-to-pin compatible
-   package not available

For more details, see the "Pin Information" section of the respective datasheets.
7. Resources

7.1 Website
At the Cypress CapSense Controllers website, you can access all the reference material discussed in this section as well as device specific datasheets and design guides.

7.2 Device Specific Design Guides
Design guides are available for each CapSense family of devices. These documents are intended for design engineers who are familiar with capacitive sensing technology and have selected a family of devices.
- CY8C20x34 CapSense® Design Guide
- CY8C20xx6A/H CapSense® Design Guide
- CY8C21x34/B CapSense® Design Guide
- CY8CMBR2044 CapSense® Design Guide

In addition to these device specific design guides, our CapSense Code Examples Design Guide is available to help you design and evaluate CapSense applications quickly using our Development Kits.

7.3 Technical Reference Manuals
Cypress has created the following technical reference manuals to provide quick and easy access to information on CapSense controller functionality including top-level architectural diagrams, register summaries, and timing diagrams.
- CY8C201xx: Register Reference Guide
- PSoC® CY8C20x66, CY8C20x66A, CY8C20x46/96, CY8C20x46A/96A, CY8C20x36, CY8C20x36A Technical Reference Manual (TRM)
- CY8CPLC20, CY8CLED16P01, CY8C29x66, CY8C27x43, CY8C24x94, CY8C24x23, CY8C24x23A, CY8C22x13, CY8C21x34, CY8C21x23, CY7C64215, CY7C603xx, CY8CNP1xx, and CYWUSB6953 PSoC® Programmable System-on-Chip TRM
- PSoC® CY8C20x24, CY8C20x34 Technical Reference Manual (TRM)

7.4 Development Kits
7.4.1 Universal CapSense Controller Kits
Universal CapSense Controller Kits feature predefined control circuitry and plug-in hardware to make prototyping and debugging easy. Programming and I²C-to-USB Bridge hardware are included for tuning and data acquisition.
- CY3280-20x34 Universal CapSense Controller
- CY3280-21x34 Universal CapSense Controller
- CY3280-20xx6A Universal CapSense Controller
- CY3280-24x94 Universal CapSense Controller
Note CY3280-20x34 Universal CapSense Controller and CY3280-21x34 Universal CapSense Controller kits are available only as a part of the CY3280-BK1 Universal CapSense Controller - Basic Kit 1.

7.4.2 Universal CapSense Module Boards

7.4.2.1 Simple Button Module Board
The CY3280-BSM Simple Button Module consists of ten CapSense buttons and ten LEDs. This module connects to any CY3280 Universal CapSense Controller Board.

7.4.2.2 Matrix Button Module Board
The CY3280-BMM Matrix Button Module consists of eight LEDs as well as eight CapSense sensors organized in a 4x4 matrix format to form 16 physical buttons. This module connects to any CY3280 Universal CapSense Controller Board.

7.4.2.3 Linear Slider Module Board
The CY3280-SLM Linear Slider Module consists of five CapSense buttons, one linear slider (with ten sensors), and five LEDs. This module connects to any CY3280 Universal CapSense Controller Board.

7.4.2.4 Radial Slider Module Board
The CY3280-SRM Radial Slider Module consists of four CapSense buttons, one radial slider (with ten sensors), and four LEDs. This module connects to any CY3280 Universal CapSense Controller Board.

7.4.2.5 Universal CapSense Prototyping Module
The CY3280-BBM Universal CapSense Prototyping Module provides access to every signal routed to the 44-pin connector on the attached controller board(s). The prototyping module board is used in conjunction with a Universal CapSense Controller board to implement additional functionality that is not part of the other single-purpose Universal CapSense Module boards.

7.4.3 CapSense Express Evaluation Kits for CY8C201xx
CY8C3218-CAPEXP series of CapSense Express evaluation kits available for CY8C201xx family of devices enables designers to replace mechanical buttons/sliders by implementing touch sensing designs with the CapSense touch sensing family, CapSense Express. With Cypress's PSoC Designer visual embedded system design tool and CapSense Express configuration tool, designers configure, monitor, and tune buttons or sliders, LEDs, and other general purpose I/Os over I2C in real time using a graphical user interface. The following are the lists of CapSense Express evaluation kits available for CY8C201xx family of devices.

- CY3218-CAPEXP1 CapSense Express Kit (Up to 10 I/O for Buttons) with CY8C20110 device
- CY3218-CAPEXP2 CapSense Express Kit (Up to 10 I/O for Sliders) with CY8C201A0 device

7.4.4 CapSense Express Evaluation Kits for CY8CMBR2044
CY3280-MBR CapSense Express kit for CY8CMBR2044 device enables designers to replace mechanical buttons with CapSense buttons. Designers can configure up to four CapSense buttons in hardware, eliminating the need for software tools, firmware development, and chip programming.

7.4.5 Evaluation Pods
PSoC EvalPods are pods that connect to the ICE In-Circuit Emulator (CY3215-DK kit) to allow debugging capability. They can also function as a standalone device without debugging capability. The EvalPod has a 28-pin DIP footprint on the bottom for easy connection to development kits or other hardware. The top of the EvalPod has prototyping headers for easy connection to the devices pins. The following are the evaluation pods available.

- CY3210-CY8C20x34 PSoC Evaluation Pod (EvalPod)
- CY3210-CY8C21x34 PSoC Evaluation Pod (EvalPod)
- CY3210-CY8C20x36/46/66 PSoC Evaluation Pod (EvalPod)
- CY3210-CY8C24x94 PSoC Evaluation Pod (EvalPod)
7.4.6 In-Circuit Emulation (ICE) Kits

The ICE pod provides the interconnection between the CY3215-DK In-Circuit Emulator via a flex cable and the target PSoC device in a prototype system or PCB via package-specific pod feet. The kit guide and quick start guide for the In-Circuit Emulator (ICE) Development Kit are available here. Following are the pods available.

- **CY3250-21X34QFN** ICE Pod Kit to debug QFN CY8C21x34 PSoC devices
- **CY3250-24x94QFN** ICE Pod Kit to debug QFN CY8C24x94 PSoC devices
- **CY3250-20246QFN** ICE Pod Kit to debug CY8C20236/46A/46AS PSoC devices
- **CY3250-20346QFN** ICE Pod Kit to debug CY8C20336/346A/346AS CapSense PSoC devices
- **CY3250-20666QFN** ICE Pod Kit to debug CY8C20636/64/666A/666AS CapSense PSoC devices
- **CY3250-20566** ICE Pod Kit to debug CY8C20536/546/566A CapSense PSoC devices
- **CY3250-20466QFN** ICE Pod Kit to debug CY8C20436/46/66/66AS CapSense PSoC devices
- **CY3250-20334QFN** ICE Pods (2) to debug QFN CY8C20334 PSoC devices

Order replacement ICE pods here.

7.5 Demonstration Kit

The **CY3235 - CapSense Proximity Detection Demonstration Kit** allows quick and easy demonstration of a PSoC CapSense-enabled device (CY8C21434) to accurately sense the proximity of a hand or finger along the length of a wire antenna. The kit also includes the **I²C-USB Bridge**, which allows hardware and software debugging of PSoC applications by seamlessly connecting the PC's USB port to your application's I²C interface.

7.6 PSoC Designer

Cypress offers an exclusive Integrated Design Environment, **PSoC Designer**. With PSoC Designer, you can configure analog and digital blocks, develop firmware, and tune your design. Applications are developed in a drag-and-drop design environment using a library of pre-characterized analog and digital functions, including CapSense. PSoC Designer comes with a built-in C compiler and an embedded programmer. A pro compiler is available for complex designs.
### 7.7 PSoC Programmer

PSoC Programmer is a flexible, integrated programming application to program PSoC devices. PSoC Programmer can be used with PSoC Designer and PSoC Creator to program any design on to a PSoC device.

PSoC Programmer provides you a hardware layer with APIs to design specific applications using the programmers and bridge devices. The PSoC Programmer hardware layer is explained in the COM guide documentation as well as example code across the following languages: C#, C, Perl, and Python.
7.8  \( \text{I}^2\text{C}-\text{to-USB Bridge Kit} \)

The **CY3240-I2USB** kit allows you to test, tune, and debug hardware and software by bridging the USB port to \( \text{I}^2\text{C} \). Populated with the **CY8C24894 PSoC** device, a wide variety of devices can be connected to the PC using this bridge including:

- EEPROMs
- Real-time clocks
- ADC/DAC converters
- LCD displays
- Regulated DC/DC converters
- Port expanders
- Other devices incorporating the \( \text{I}^2\text{C} \) interface

The number of devices that can be connected is constrained only by the \( \text{I}^2\text{C} \) address limit and physical ability of the \( \text{I}^2\text{C} \) bus.

7.9  **Debugging/Data Viewing Tools**

Software tools are available for data viewing, debugging, and tuning CapSense applications. These tools can help you monitor critical data, such as raw counts and CapSense parameters.

The debugging and data viewing tools are:

- Bridge Control Panel
- MultiChart

For more details on the tools, see the application note **AN2397 – CapSense Data Viewing Tools**.

7.9.1  **Bridge Control Panel**

Bridge Control Panel is a software tool used with CY3240 USB-\( \text{I}^2\text{C} \) Bridge to enable communication with \( \text{I}^2\text{C} \) slave devices. The software tool is used to configure \( \text{I}^2\text{C} \) devices as well as acquire and process data received from \( \text{I}^2\text{C} \) slave devices. The Bridge Control Panel helps in optimizing, debugging, and calibrating the target applications.

![Bridge Control Panel](image)

7.9.2  **MultiChart**

**MultiChart** is a simple PC tool for real-time CapSense data viewing and logging. The application allows you to view data from up to 48 sensors, save and print charts, and save data for later analysis in a spreadsheet.

![MultiChart](image)
Figure 7-4. MultiChart User Interface
7.10 Design Support

Cypress has several support channels to ensure the success of your CapSense design.

- **Code Examples** – Our vast collection of code examples will help get your design up and running fast.
- **Knowledge Base Articles** – Browse technical articles by product family or perform a search on various CapSense topics.
- **White Papers** and **Technical Articles** – Learn about advanced capacitive touch interface topics.
- **Cypress Developer Community** – Connect with the Cypress technical community and exchange information.
- **Video Library** – Get up to speed with tutorial videos.
- **Quality and Reliability** – Cypress is committed to complete customer satisfaction. At our Quality website, you can find reliability and product qualification reports.
- **Cypress Design Partner Program** – An expansion of our engineering capabilities providing customers with access to design services and solutions from trusted and capable partners.
- **Cypress Developer Community** – A very active online community to discuss technical issues.
- **Technical Support** – Excellent technical support is available online.
8. Appendix

8.1 Appendix A: Springs

8.1.1 Finger-Introduced Capacitance

This section gives the influence of various physical parameters on finger-introduced capacitance in a CapSense design with springs.

- Influence of overlay thickness on Finger Touch added Capacitance (FTC) with springs is similar to that with solid sensors

Figure 8-1. FTC versus Overlay Thickness

- Influence of height on FTC

Figure 8-2. FTC versus Spring Height
8.1.2 Mounting Springs to the PCB

Figure 8-5 shows an example of spring mounting. This section discusses how to design spring sensors. Because springs have higher side sensitivity, the neighboring spring sensors must be placed as far as possible from each other to prevent false detections. Add a comparison level if the sensor pitch is small.

The requirements for the sensitive area of a spring are the same as the requirements for solid buttons. When using thick overlays, the spring diameter must be larger than the overlay thickness by at least 2 or 3 times. The distance between the PCB and the overlay must be 5 mm or more.
Figure 8-6 shows examples of footprints for springs. Do not place solid grounds under the springs as this complicates the spring soldering and increases the native capacitance of the sensors.

8.1.3 CapSense and Mechanical Button Combination
The hollow space inside a spring can also be used as a mechanical button, as shown in Figure 8-7.

Touching such a button only triggers the sensor, while pressing the button activates both the sensor and mechanical button. In this case, preparatory actions such as backlighting, prompt showing, and others are possible only if the sensor works. The final action is performed when both buttons work. For example, in a GPS navigation system, touching a button shows only a hint and pressing the button takes an action.

8.1.4 Design Examples
Figure 8-8 and Figure 8-9 show project demonstrator examples for white goods applications.
### 8.2 Document Revision History

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<thead>
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<th>Revision</th>
<th>Issue Date</th>
<th>Origin of Change</th>
<th>Description of Change</th>
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<td>**</td>
<td>12/17/2010</td>
<td>SSHH</td>
<td>New guide</td>
</tr>
<tr>
<td>*A</td>
<td>03/04/2011</td>
<td>SSHH</td>
<td>Multiple chapter enhancements for content and reader clarity</td>
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<td>*B</td>
<td>08/16/2011</td>
<td>SSHH/BVI</td>
<td>Multiple section and table updates</td>
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<td>*C</td>
<td>12/07/2011</td>
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<td>Multiple chapter enhancements for content clarity</td>
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<td>*E</td>
<td>07/19/2012</td>
<td>ZINE</td>
<td>Updated sample schematics and layouts. Included information on layout/trace routing guidelines for $C_{MOD}, R_b$ pin.</td>
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<td>08/31/2012</td>
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<td>Updated references to external documents</td>
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<td>*G</td>
<td>10/16/2012</td>
<td>ZINE</td>
<td>Updated Section 3.1 (Overlay Selection) and moved Table 3-1 to Section 3.2.</td>
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<td>Updated Section 3.1. Added Section 3.7.13.</td>
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